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STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 2 of 2

**Complete If Known**

Application Number	10/614,956
Filing Date	July 8, 2003
First Named Inventor	Victor Suen
Group Art Unit	
Examiner Name	
Attorney Docket Number	02-6052

**OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
JC		Micron 288 Mb SIO Reduced Latency (RLDRAM II) Rev. 2, Pub 04/03, Micron Technology, Inc. 2003 pp. 1-46, Boise, Idaho	
JC		Micron TN-46-06, Technical Note Termination for Point-to-Point Systems, pp. 1-6-Rev. 06/01, 2001 Micron Technology, Inc., Boise, Idaho	

Examiner Signature	/Jason Crawford/	Date Considered	02/16/2007
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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